

**IN THE SPECIFICATION**

Please replace paragraph 1 of Specification page 1 with the following replacement paragraph:

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The present invention is related to the following co-pending and commonly assigned U.S. Patent Application Serial No. 10/035,607 filed on 12/28/2001 now Issued U.S. Patent No. \_\_\_\_\_ on \_\_\_\_\_ (date) ~~(112056-0031)~~ titled, *Row-Diagonal Parity Technique for Enabling Efficient Recovery from Double Failures in a Storage Array*, which was filed on even date herewith and which application is hereby incorporated by reference as though fully set forth herein.

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Please replace paragraph 1 on Specification page 14 with the following replacement paragraph:

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In the illustrative embodiment described herein, two concatenations of sub-arrays 310 are considered, one based on "row-diagonal" encoding and the other based on conventional EVENODD encoding. Row-diagonal (R-D) encoding is a parity technique that provides double failure parity correcting recovery using row and diagonal parity in a disk array. Two disks of the array are devoted entirely to parity while the remaining disks hold data. The contents of the array can be reconstructed entirely, without loss of data, after any one or two concurrent disk failures. An example of a R-D parity technique that may be advantageously used with the present invention is disclosed in the co-pending and commonly-owned U.S. Patent Application Serial No. 10/035,607 filed on 12/28/2001 now Issued U.S. Patent No. \_\_\_\_\_ on \_\_\_\_\_ (date) ~~(112056-0031)~~ titled *Row-Diagonal Parity Technique for Enabling Efficient Recovery from Double Failures in a Storage Array*.

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